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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Gregory J. Faanes et al.

Title: SYSTEM AND METHOD FOR PROCESSING MEMORY INSTRUCTIONS

Docket No.: 1376.711US1

Serial No.: 10/643,577

Filed: August 18, 2003

Due Date: January 27, 2008

Examiner: Shane M. Thomas

Group Art Unit: 2186

MS Amendment

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

We are transmitting herewith the following attached items (as indicated with an "X"):

- Return postcard.
- Amendment and Response Under 37 C.F.R. 1.111 (18 pgs.).
- Petition for Extension of Time (1 pg.), with authorization to charge fee to Deposit Acct. 19-0743.
- Communication Concerning Related Applications (2 pgs.).
- Supplemental Information Disclosure Statement (2 pgs.), Form 1449 (4 pgs.), and copies of 1 cited documents; with authorization to charge fee to Deposit Acct. 19-0743.

If not provided for in a separate paper filed herewith, Please consider this a PETITION FOR EXTENSION OF TIME for sufficient number of months to enter these papers and please charge any additional fees or credit overpayment to Deposit Account No. 19-0743.

SCHWEGMAN, LUNDBERG & WOESSNER, P.A.

Customer Number 21186

By: Thomas F. Brennan
Atty: Thomas F. Brennan
Reg. No. 35,075

CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: MS Amendment, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this 24 day of January, 2008.

✓

CANDIS BUENDING

Name

Signature

SCHWEGMAN, LUNDBERG & WOESSNER, P.A.

(GENERAL)

S/N 10/643,577



PATENT

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Serial No.: 10/643,577 Group Art Unit: 2186
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COMMUNICATION CONCERNING RELATED APPLICATION(S)

MS Amendment

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Applicants would like to bring to the Examiner's attention the following related application(s) in the above-identified patent application:

<u>Serial/Patent No.</u>	<u>Filing Date/Issue Date</u>	<u>Attorney Docket</u>	<u>Title</u>
10643742	August 18, 2003	1376.697US1	DECOUPLED STORE ADDRESS AND DATA IN A MULTIPROCESSOR SYSTEM
10643586	August 18, 2003	1376.699US1	DECOUPLED SCALAR/VECTOR COMPUTER ARCHITECTURE SYSTEM AND METHOD (As Amended)
10643585	August 18, 2003	1376.700US1	LATENCY TOLERANT DISTRIBUTED SHARED MEMORY MULTIPROCESSOR COMPUTER
10235898 6922766	September 4, 2002	1376.710US1	REMOTE TRANSLATION MECHANISM FOR A MULTI-NODE SYSTEM
10643754	August 18, 2003	1376.724US1	RELAXED MEMORY CONSISTENCY MODEL
10643727	August 18, 2003	1376.731US1	METHOD AND APPARATUS FOR INDIRECTLY ADDRESSED VECTOR LOAD-ADD-STORE ACROSS MULTIPROCESSORS
10643741	August 18, 2003	1376.733US1	MULTISTREAM PROCESSING MEMORY-AND BARRIER-SYNCHRONIZATION METHOD AND APPARATUS

Continuations and divisionals may be later filed on the cases listed above, or cited to the Examiner in any previous Communication Concerning Related Applications. Applicants request that the Examiner review all continuations and divisionals of the above-listed or previously-cited patent applications before allowing the claims of the present patent application.

Respectfully submitted,

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Date January 23, 2008 By Thomas J. Brennan
Thomas F. Brennan
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